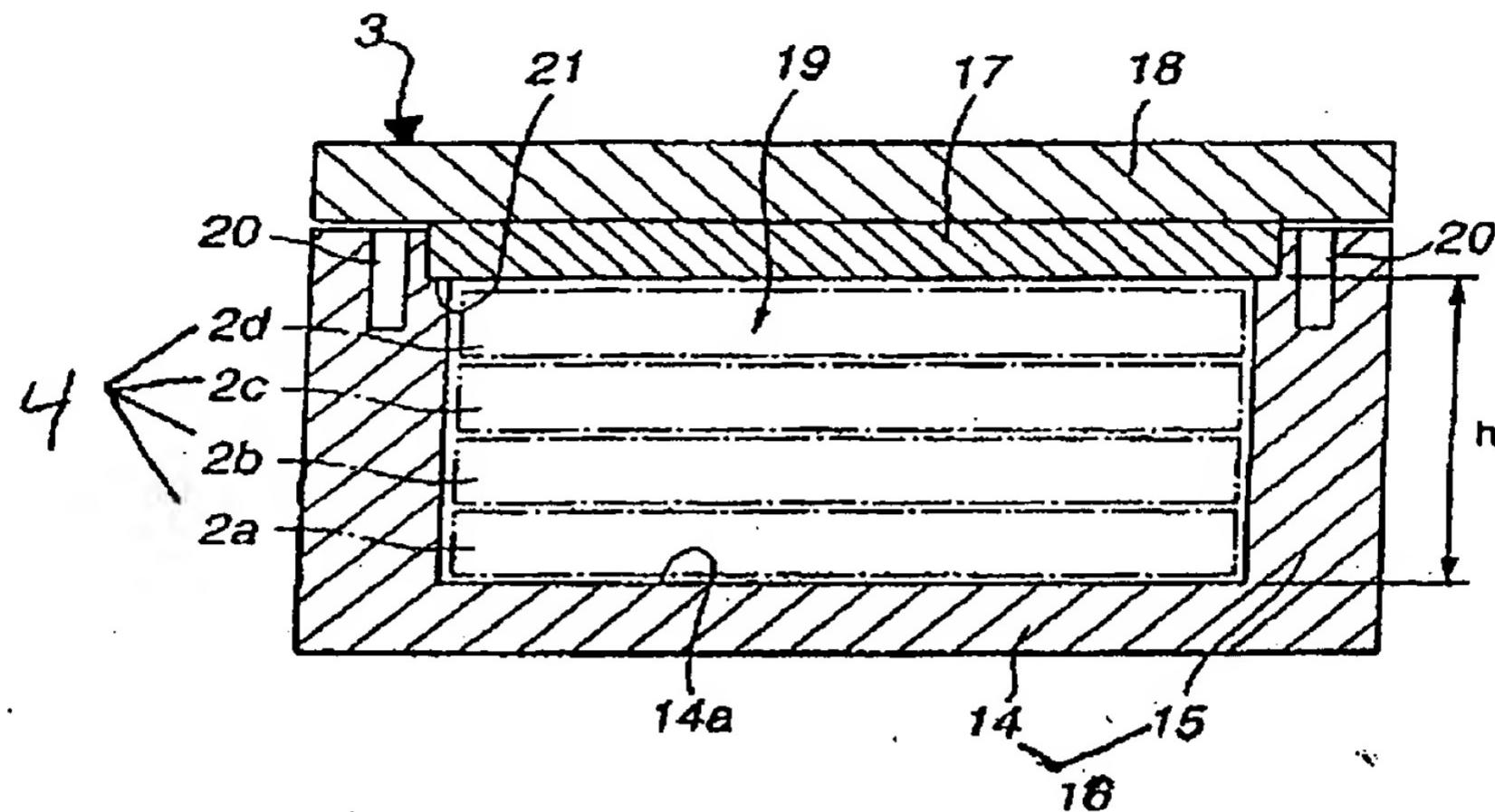
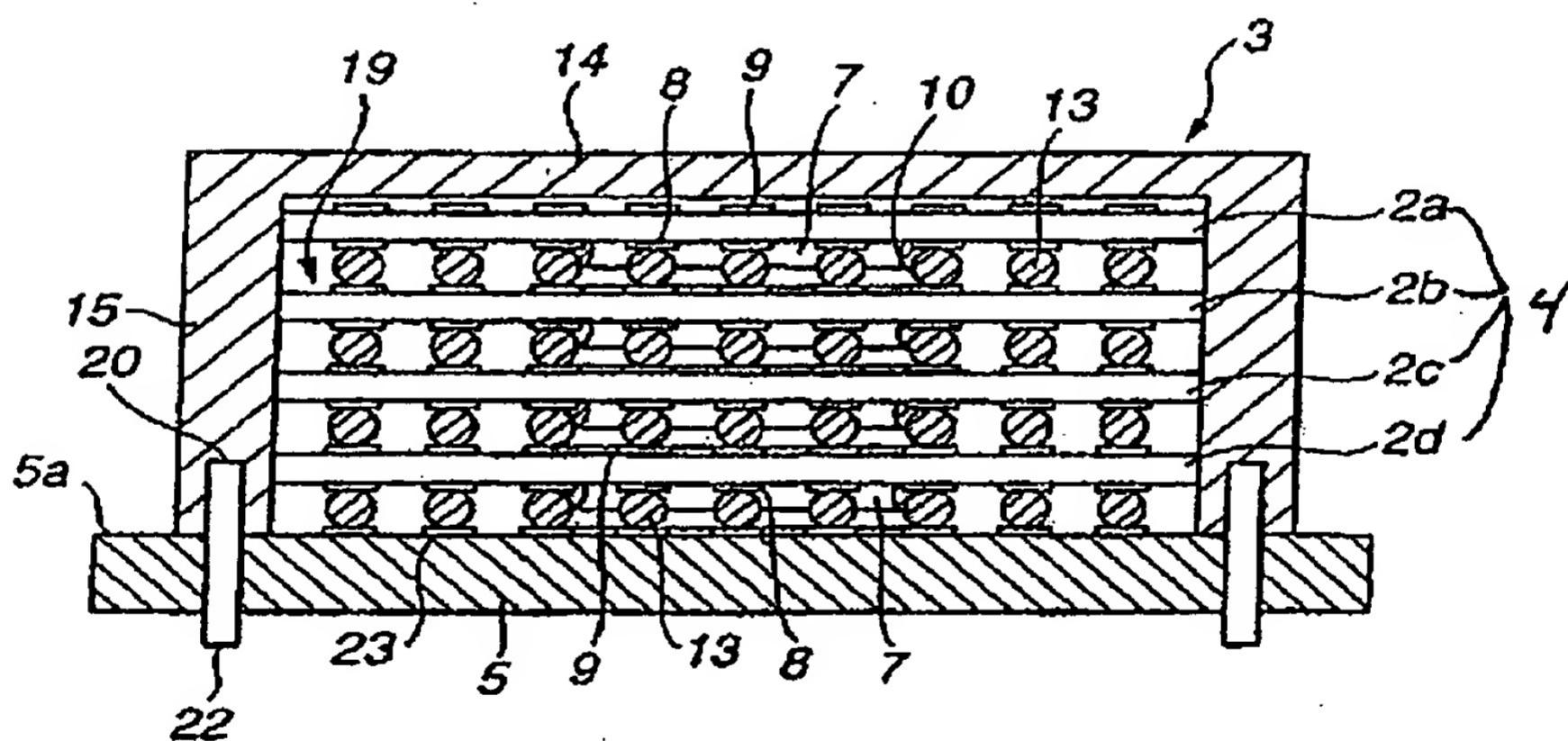


ASSEMBLY JIG AND MANUFACTURING METHOD OF MULTILAYER SEMICONDUCTOR DEVICE
REPLACEMENT DRAWINGS
Inventor: Yoshiyuki Yanagisawa et al.

3/4

**FIG.3****FIG.4**